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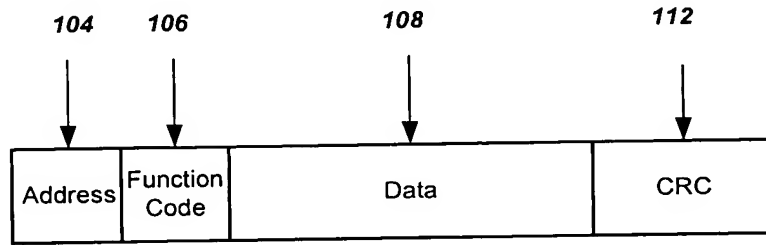


Figure 1a

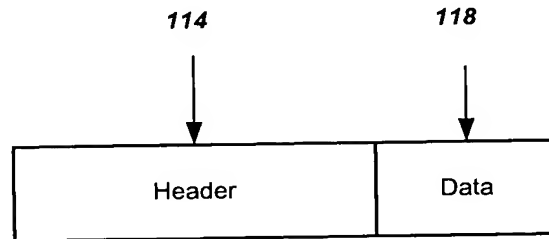


Figure 1b

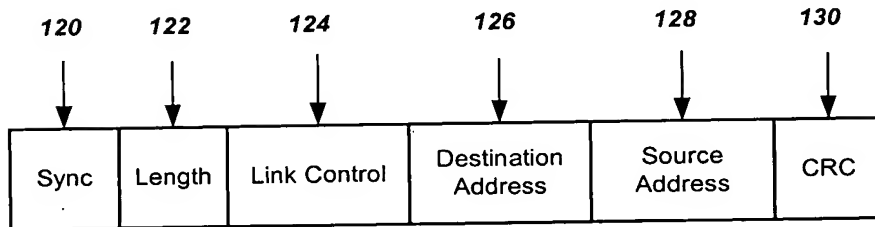


Figure 1c

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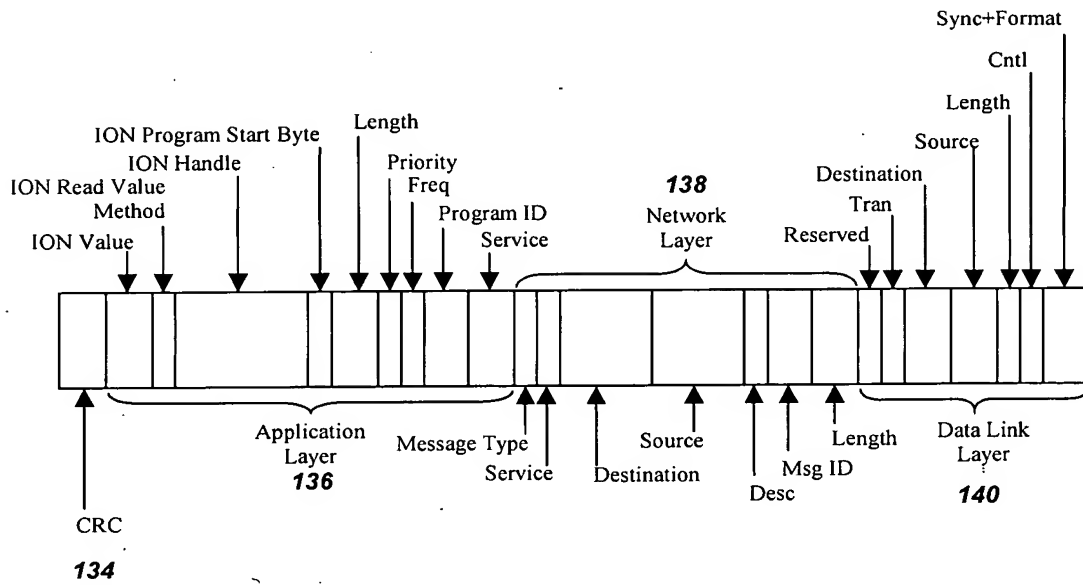


Figure 1d

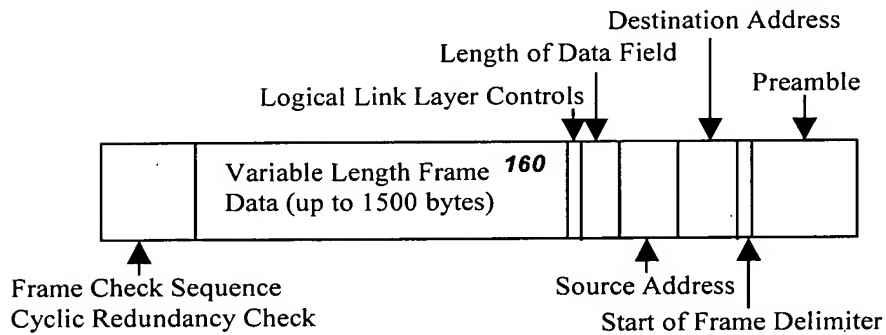


Figure 1e

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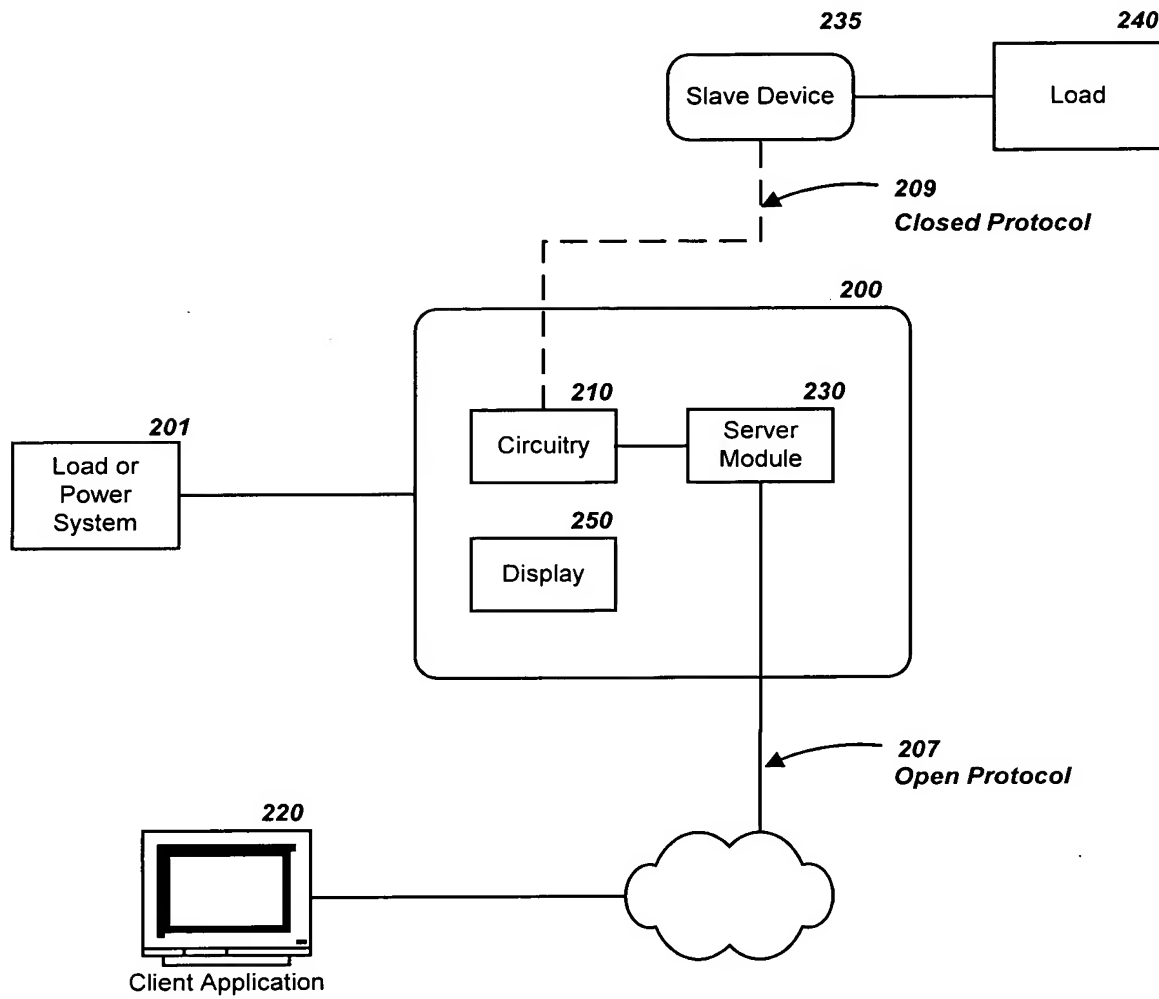


Figure 2

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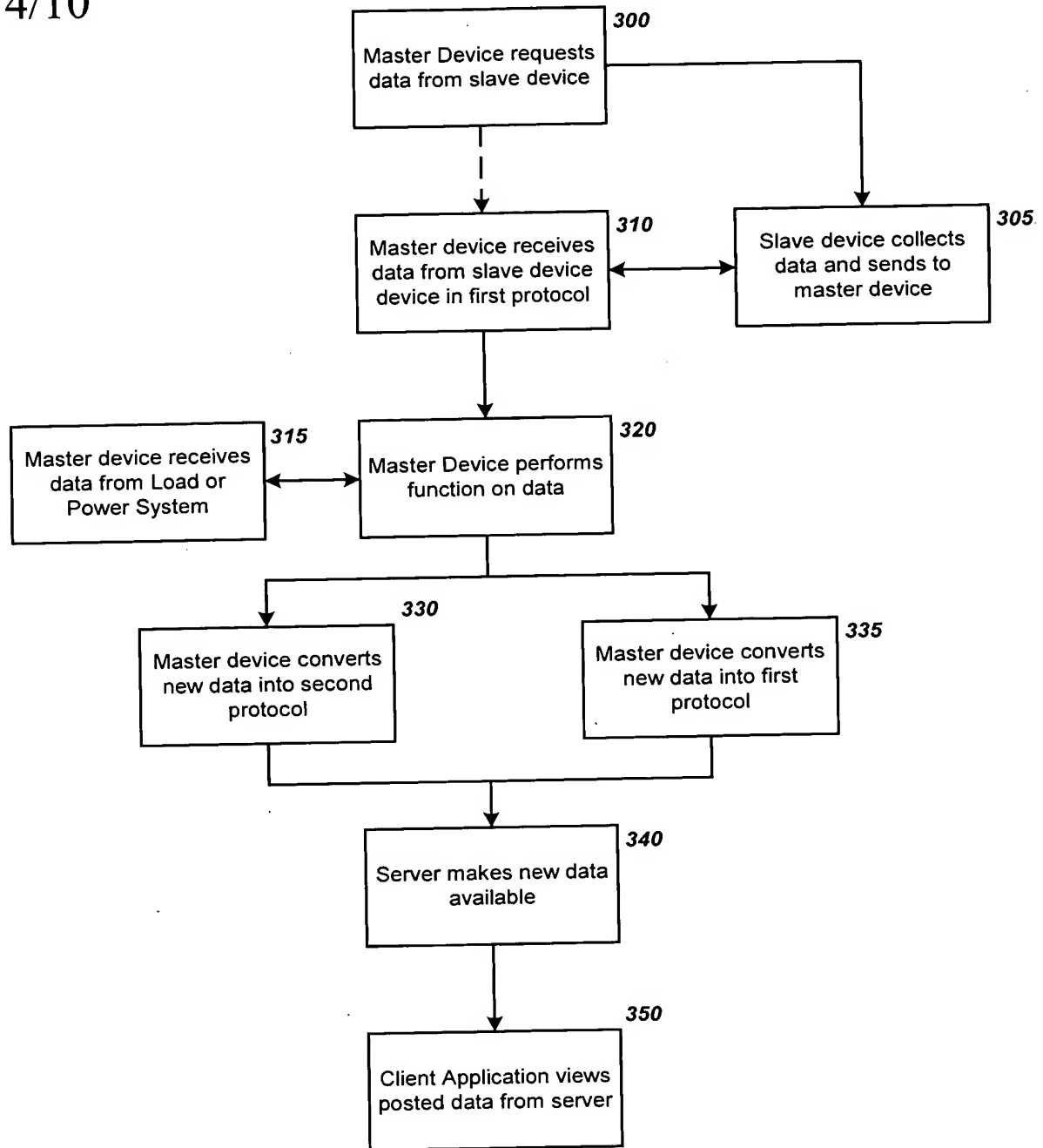


Figure 3

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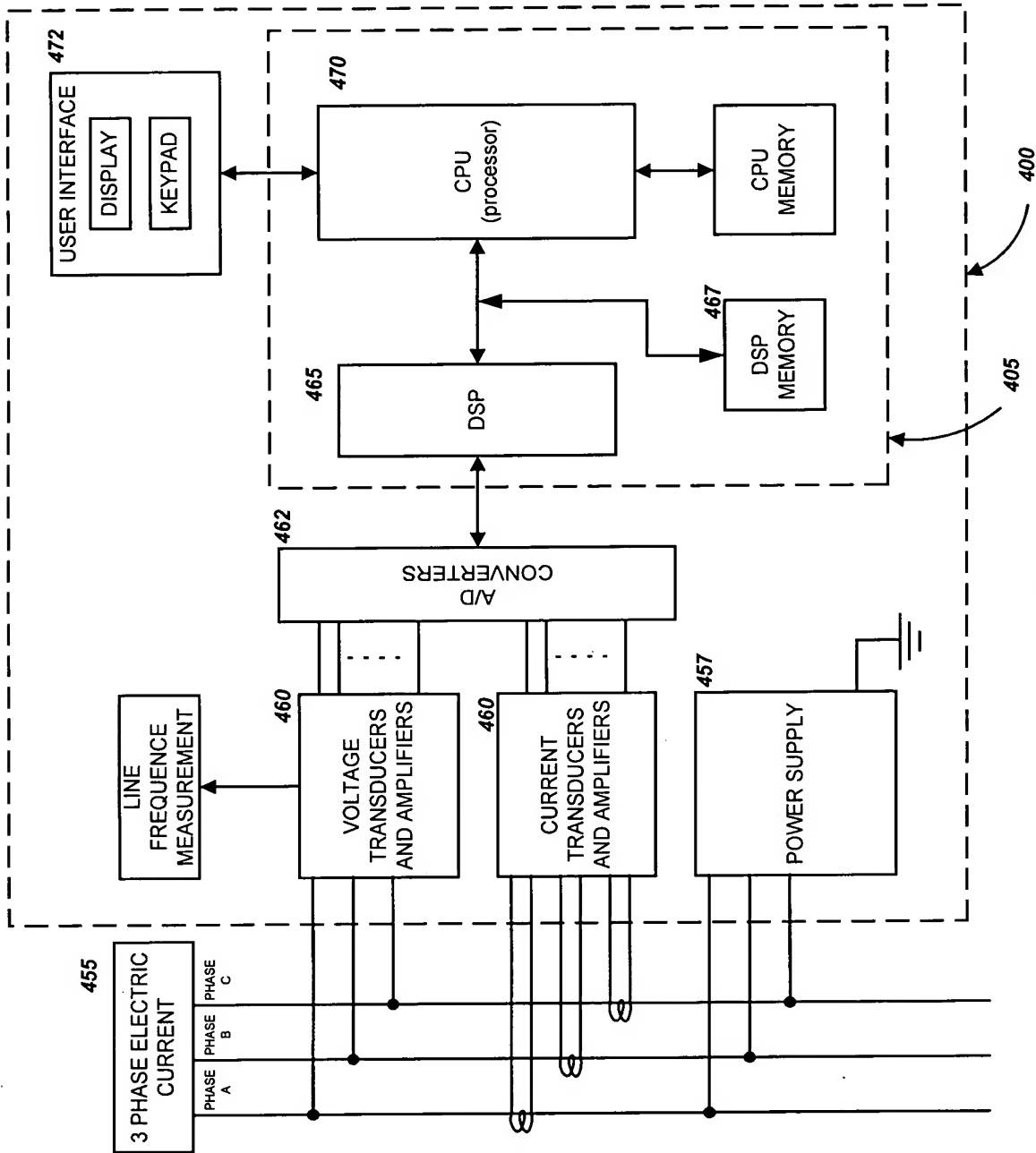


Figure 4a

FIG. 4a is a block diagram of an intelligent electronic device (400) in accordance with the present invention.

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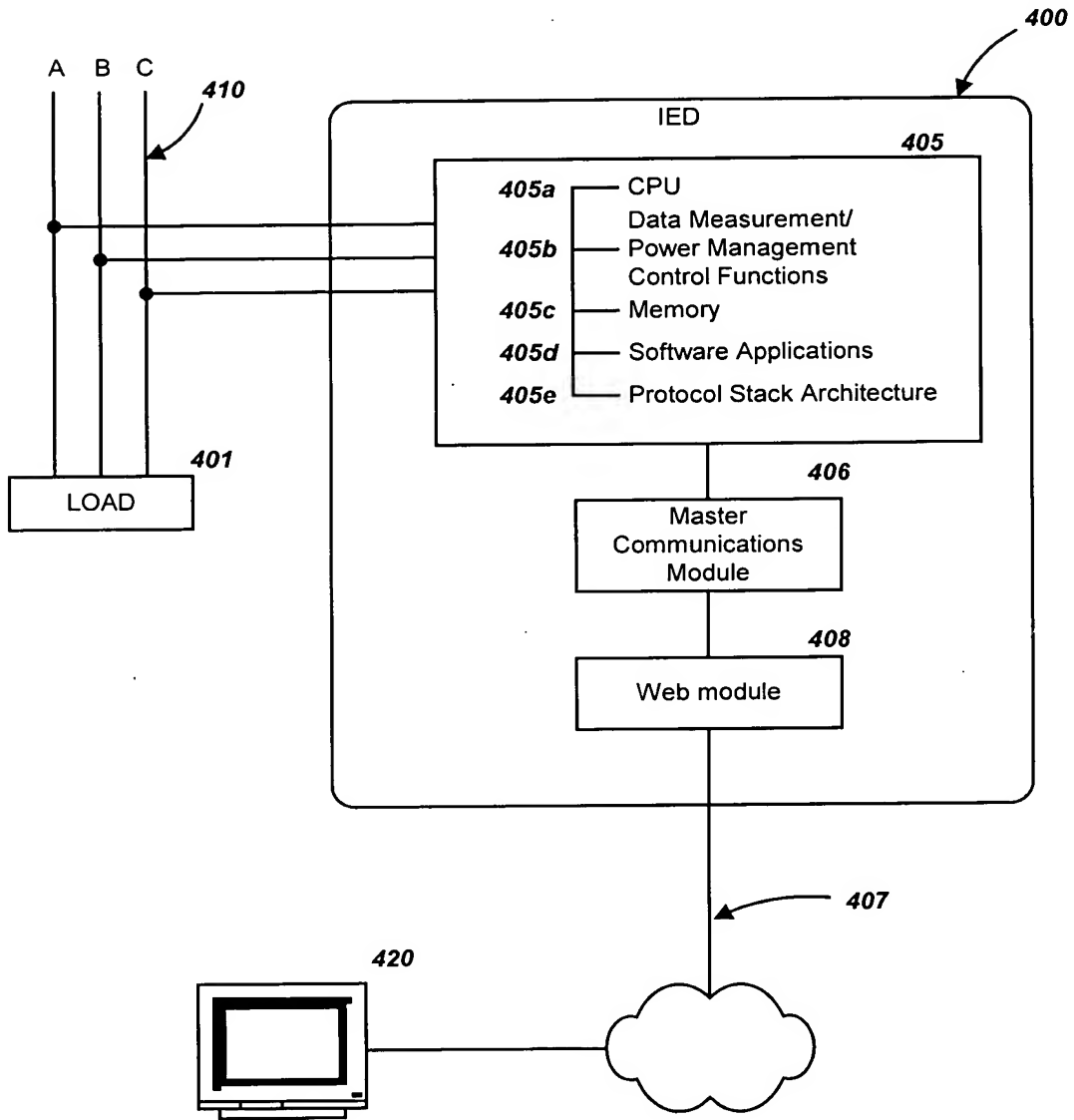


Figure 4b

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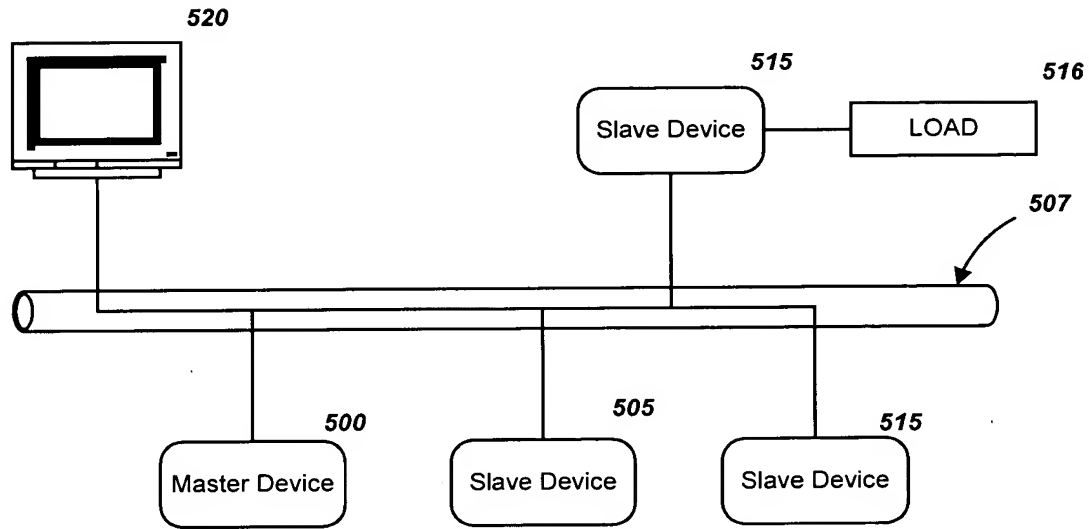


Figure 5

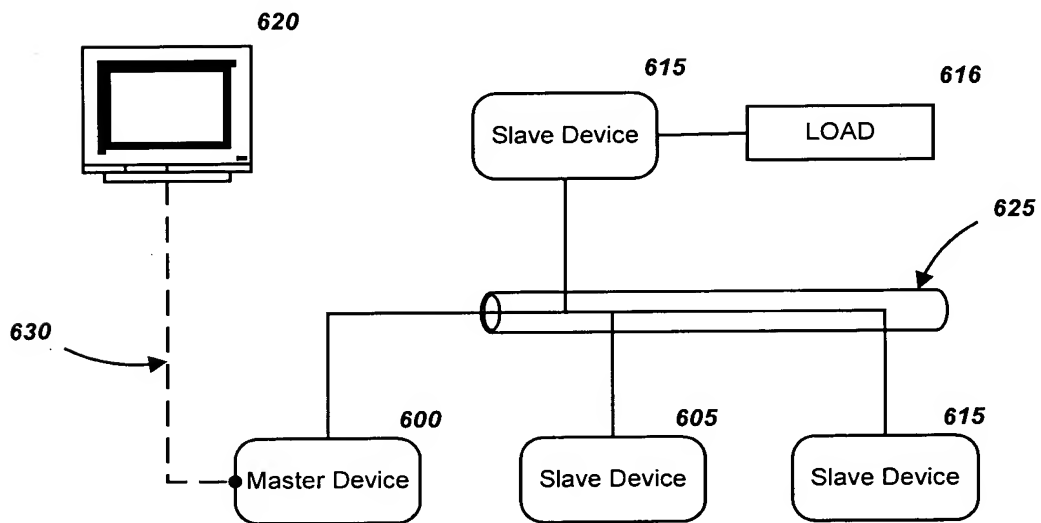


Figure 6

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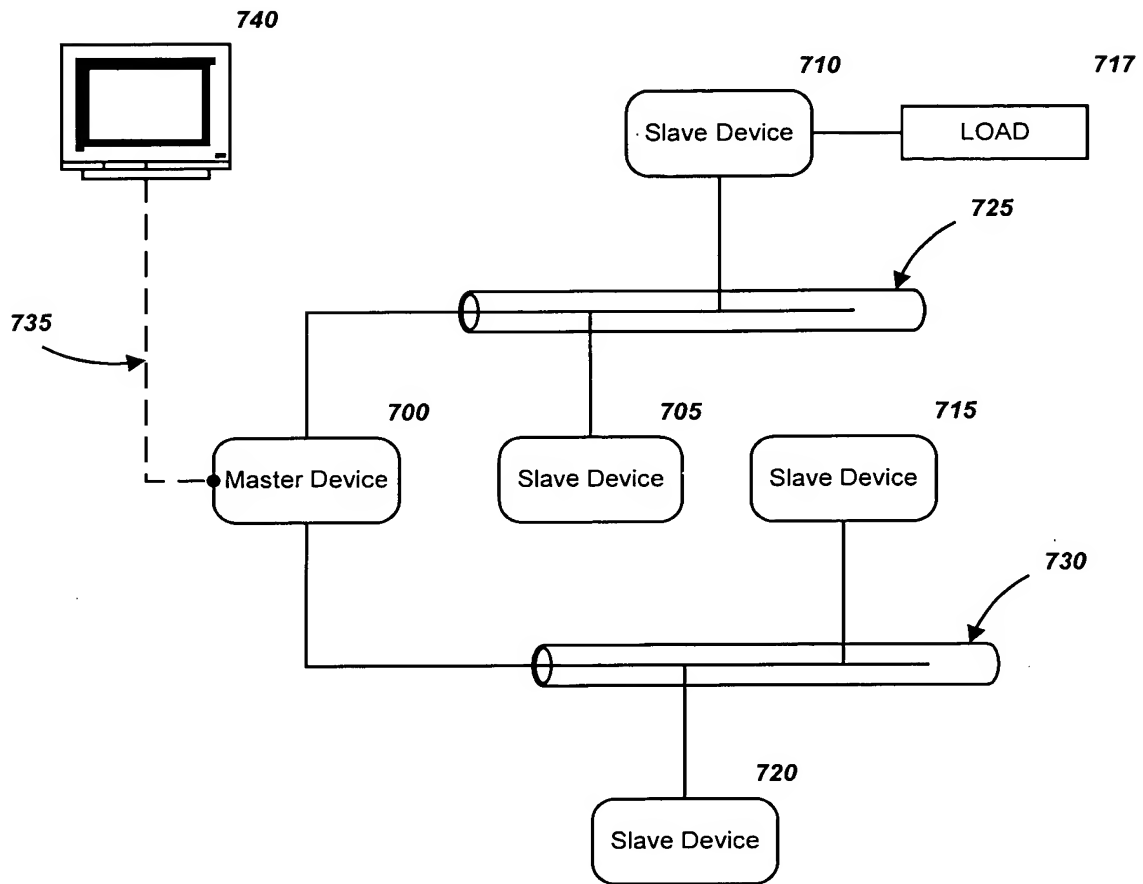


Figure 7



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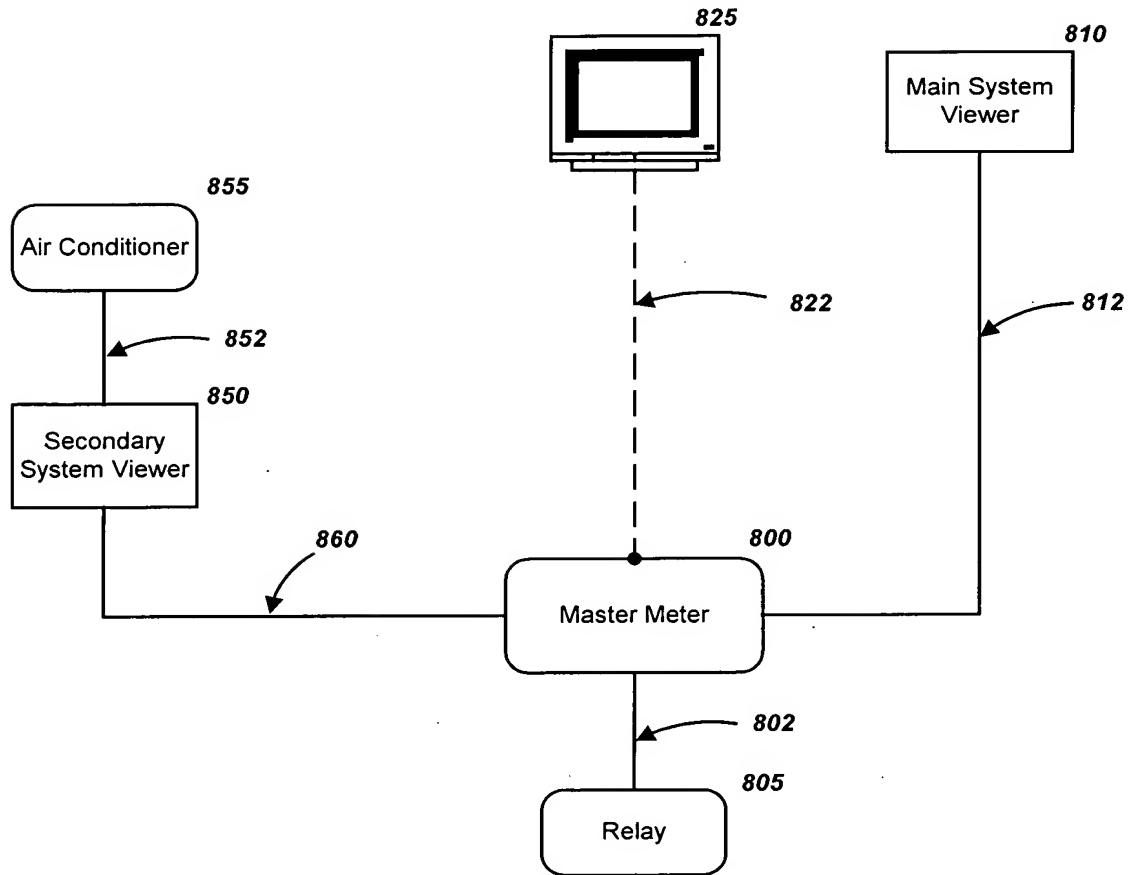


Figure 8

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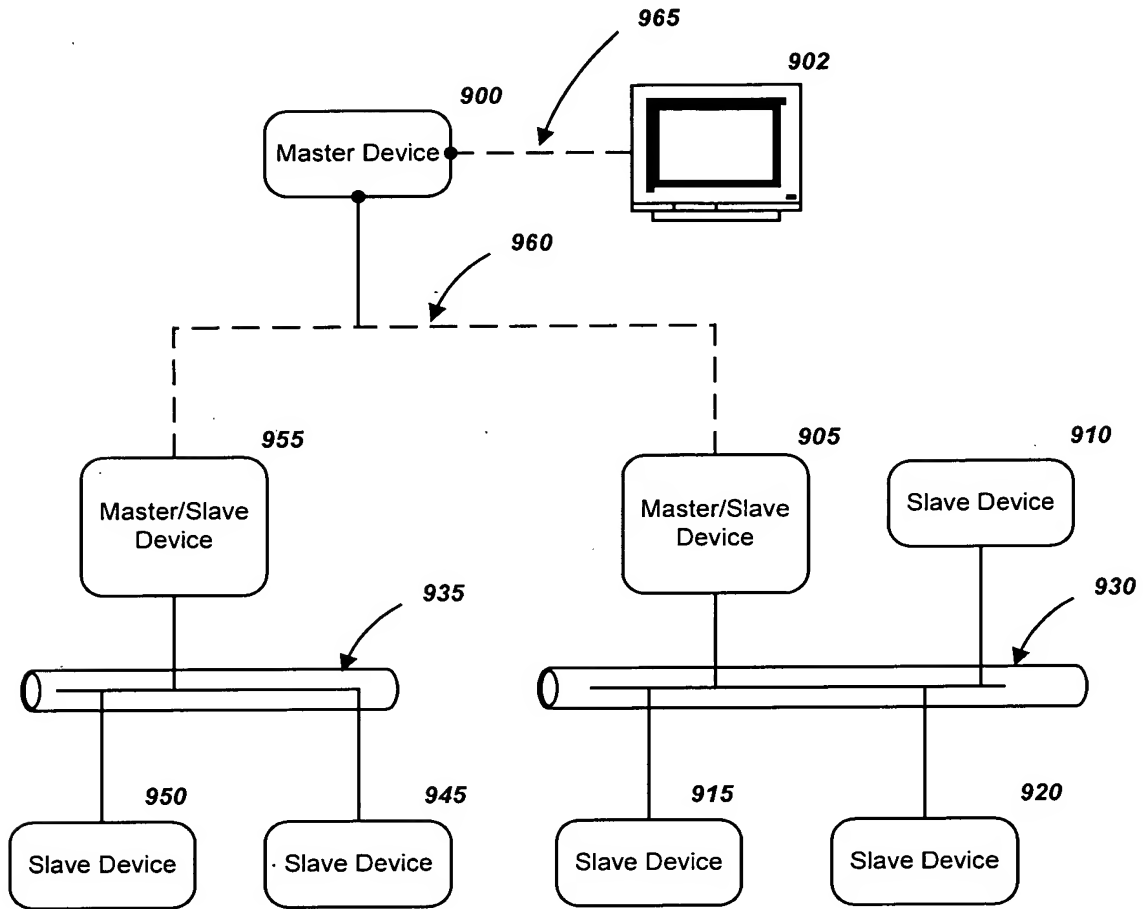


Figure 9